

FUTURE INSTITUTE OF ENGINEERING AND MANAGEMENT

DEPT. OF ELECTRONICS AND COMMUNICATIONS ENGINEERING

ECE INSIGHTS

NEWSLETTER || NOVEMBER || 2025

VOICE OF THE DEPARTMENT



PROF. ANUPAM PATRA
ASSISTANT PROFESSOR

The Department of Electronics and Communication Engineering (ECE) at FIEM stands as a proud beacon of excellence, earning recognition across West Bengal and beyond. In today's technology-driven era, ECE is more than just a field of study—it's the heartbeat of innovation, driving advancements

in semiconductor technology, solar energy, and countless other cutting-edge domains where opportunities are boundless.

At FIEM, our ECE students don't just learn—they innovate, participate, and grow. Through engaging seminars, hands-on research, and vibrant cultural activities, they evolve into dynamic individuals ready to take on global challenges. Our alumni, now excelling across India and abroad, stand as living proof of our department's legacy of success. With the knowledge, skills, and values nurtured here, every ECE student at FIEM holds the power to shape the future and make a lasting mark on the world.

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Departmental Page**



SCAN ME

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Faculty' FDP

Prof. (Dr) Tarun Kumar Das

Name of the Faculty Development Program: Session Speaker at the "4th International conference on Advances in Science, Engineering & Technology (ICASET)"

Date of the Faculty Development Program: 15th & 16th November, 2025

Organized By: Panipat Institute of Engineering and Technology, Haryana, Kamaraj College of Engineering & Technology, India and IFERP Academy.



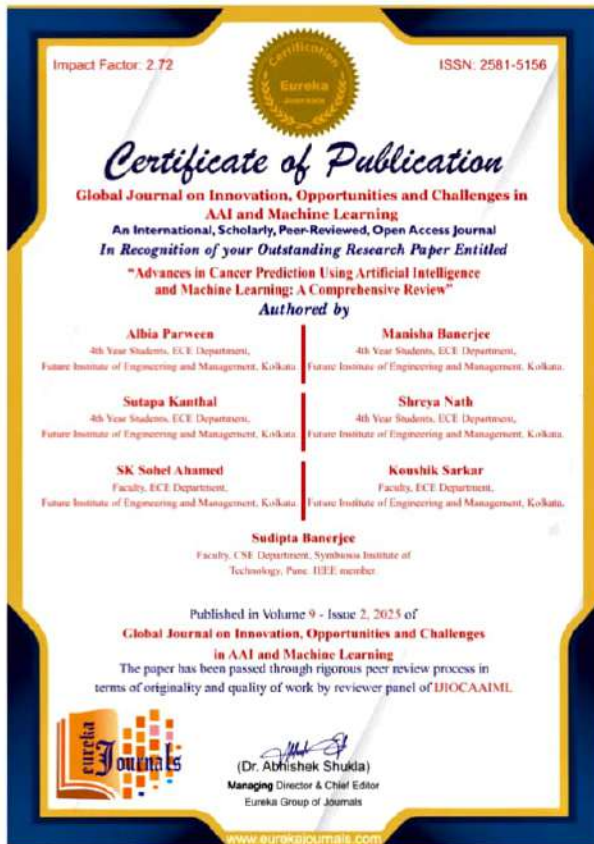
PROF. (DR) TARUN KUMAR DAS
ASSOCIATE PROFESSOR



Prof. (Dr) Tarun Kumar Das being felicitated by the HOD, Dept. of ECE, Heritage Institute of Technology, Kolkata on 12.11.25, as a guest judge for the event "AI-In-Hardware: Smart System Design Challenge 2025" organized by IEEE MTT-S HITK Student Branch Chapter

Co-curricular and extra curricular activities By Students and Faculties

Journal Publication



MANISHA BANERJEE
ECE, 4TH YEAR



PROF. SOHEL AHAMED,
ASSISTANT PROFESSOR



DR. KUSHIK SARKAR,
ASSISTANT PROFESSOR



SUTAPA KANTHAL
ECE, 4TH YEAR



SHREYA NATH
ECE, 4TH YEAR



ALBIA PARWEEN
ECE, 4TH YEAR

The Department of Electronics and Communication Engineering proudly congratulates our brilliant students and esteemed faculty members for their successful publication in the Journal on **Advances in Cancer Prediction Using Artificial Intelligence and Machine Learning: A Comprehensive Review.**

Their research paper titled “**Scope and Potential of Research on Robotics: A Short Review**” has been published in **Volume 9, Issue 2, 2025**, reflecting their dedication, hard work, and passion for innovation in the field of **AAI and Machine Learning.**

Heartiest Congratulations to the Authors:

- **Albia Parween**– 4th Year, ECE
- **Manisha Banerjee** – 4th Year, ECE
- **Sutapa Kanthal** – 4th Year, ECE
- **Shreya Nath** – 4th year, ECE
- **SK Sohel Ahamed** – Faculty, ECE
- **Koushik Sarkar** – Faculty, ECE
- **Sudipta Banerjee** – Faculty, CSE, Symbiosis Institute of Technology (IEEE Member)

This achievement highlights the growing research culture in our department and motivates our students and faculty to continue exploring cutting-edge technologies and contributing to the global academic community.

We extend our warmest congratulations once again and wish them continued success in their future research endeavors!

Journal Publication



PROF. (DR.) SUMEDHA DASGUPTA
ASSISTANT PROFESSOR



PROF. (DR.) GARGI BHATTACHARYA
ASSOCIATE PROFESSOR



ABARNA DUTTA



ADRIJA BHATTACHARJEE



ANURAN SEN



DEBARPITA PRAMANIK

Research Proposes Optimized DG-TFET for Digital Applications

A paper by Gargi Chakraborty, Sumedha Dasgupta, Abarna Dutta, Adrija Bhattacharjee, Anuran Sen, and Debarpita Pramanik investigates Channel Thickness Engineering for Enhanced Digital Performance in Double-Gate Tunnel Field Effect Transistor (DG-TFET). The study found that while the 8 nm channel thickness shows better OFF-state characteristics and the 16 nm device has a better ON-OFF ratio and lower intrinsic delay, the 12 nm DG-TFET is proposed as the ideal alternative for next-generation digital applications due to its balanced performance metrics.

The Department of Electronics and Communication Engineering at Future Institute of Engineering and Management extends its warmest congratulations and appreciation to the authors of the recently published paper, "Channel Thickness Engineering for Enhanced Digital Performance in Double-Gate Tunnel Field Effect Transistor." We are incredibly proud of the collaborative effort shown by our faculty, Dr. Gargi Chakraborty (Associate Professor) and Dr. Sumedha Dasgupta (Assistant Professor), along with our talented students: Abarna Dutta, Adrija Bhattacharjee, Anuran Sen, and Debarpita Pramanik. Your rigorous research and contribution to the field of next-generation digital applications, specifically in advancing the Double-Gate Tunnel Field Effect Transistor (DG-TFET), reflects the high standard of academic excellence we strive for. This achievement is an inspiration to your peers and the entire department. Congratulations on this significant accomplishment!

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